

257 723  
Class Subclass  
ISSUE CLASSIFICATION

5841193



UTILITY SERIAL NUMBER 0865 0628 PATENT DATE NOV 24 1998 PATENT NUMBER

SERIAL NUMBER 08/650,628 FILING DATE 05/20/96 CLASS 257 SUBCLASS 723 GROUP ART UNIT 2811 EXAMINER Potte

APPLICANTS CHARLES W. EICHELBERGER, SCHENECTADY, NY.

\*\*CONTINUING DATA\*\*\*\*\* None

VERIFIED

\*\*FOREIGN/PCT APPLICATIONS\*\*\*\*\* None

VERIFIED

FOREIGN FILING LICENSE GRANTED 06/25/96

\*\*\*\*\* SMALL ENTITY \*\*\*\*\*

Foreign priority claimed ☐ yes ☒ no 35 USC 119 conditions met ☐ yes ☒ no AS FILED STATE OR COUNTRY NY SHEETS DRWGS. 11 TOTAL CLAIMS 59 INDEP. CLAIMS 1 FILING FEE RECEIVED \$843.00 ATTORNEY'S DOCKET NO. 1109.001

Verified and Acknowledged Examiner's Initials KEVIN P. RADIGAN HESLIN AND ROTHENBERG 5 COLUMBIA CIRCLE ALBANY NY 12203

CERTIFICATE

JUN 15 1999

RECORDED IN FILE

TITLE SINGLE CHIP MODULES, REPAIRABLE MULTICHIP MODULES, AND METHODS OF FABRICATION THEREOF

U.S. DEPT. of COMMERCE • Patent and Trademark Office-PCT-436L (rev. 7-94)

PARTS OF APPLICATION  
FILED SEPARATELY

Sharon Surles  
Applications Examiner

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

6/8/98  
Roy Potter  
Assistant Examiner

Total Claims 30 Print Claim 1

ISSUE FEE

DRAWING

Amount Due 4660 Date Paid 9/3/98

Sheets Drwg. 11 Figs. Drwg. 26 Print Fig. 4

TOM THOMAS  
SUPERVISORY PATENT EXAMINER

ISSUE BATCH NUMBER 584

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PREPARED FOR ISSUE 6/9/98

Label  
Area

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PATENT NUMBER

ORIGINAL CLASSIFICATION

CLASS

SUBCLASS

257

723

APPLICATION SERIAL NUMBER

08/650,628

APPLICANT'S NAME (PLEASE PRINT)

EICHELBARGER

IF REISSUE, ORIGINAL PATENT NUMBER

CROSS REFERENCE(S)

CLASS

SUBCLASS

(ONE SUBCLASS PER BLOCK)

257

773

787

INTERNATIONAL CLASSIFICATION

401 23 34

GROUP  
ART UNIT

2822

ASSISTANT EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

ROY POTTER

PRIMARY EXAMINER (PLEASE STAMP OR PRINT FULL NAME)

TOM THOMAS

ISSUE CLASSIFICATION SLIP

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE